



#7/8
2/2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/756,971
Filing Date January 9, 2001
Inventor Salman Akram
Assignee Micron Technology, Inc.
Group Art Unit 2812
Examiner Jones, J.
Attorney's Docket No. MI22-1572
Title: Methods of Forming Board-On-Chip Packages

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

To: Assistant Commissioner for Patents
Washington, D.C. 20231

From: David G. Latwesen, Ph.D.
(Tel. 509-624-4276; Fax 509-838-3424)
Wells St. John P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828

RECEIVED
FEB - 5 2002
TC 2000 MAIL ROOM

Dear Sir:

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449 and copies of which are attached.

Citation of these references is respectfully requested.

A check in the amount of \$180.00 is enclosed to cover the fee specified under 37 C.F.R. § 1.17(p).

OFFICE OF INTELLECTUAL PROPERTY AND EXAMINATION (OIP&E)

U.S. PATENT

180.00 (P)

Respectfully submitted,

Dated:

1/29/02

By:

David G. Latwesen, Ph.D.
Reg. No. 38,533